TED (15) 6041
(Revision - 2015)

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Reg. No……
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DIPLOMA EXAMINATION IN ENGINEERING/TECHNOLOGY/ MANAGEMENT/COMMERCIAL PRACTICE, NOVEMBER – 2020

ADVANCED MICROPROCESSORS

[Maximum Marks: 75] [Time: 2.15 Hours]

PART-A

(Answer ***any three*** questions in one or two sentences. Each question carries 2 marks)

I. 1. List the segment Registers in Intel 8086.

2. Define addressing mode.

3. Define assembler directive.

4. List the operating modes of 80386.

5. Define core. (3 x 2 = 6)

PART-B

(Answer ***any four*** of the following questions. Each question carries ***6*** marks)

II 1. List the main features of Intel 8086.

2. Draw the flag Register of 8086.

3. Explain Intel predefined interrupts in 8086.

4. List the features of Pentium processor.

5. Describe the paging mechanism in 80386.

6. List the advantages of multi core technology.

7. Classify the instructions of 8086. (4 x 6 = 24)

PART-C

(Answer ***any of the three units*** from the following. Each full question carries 15 marks)

UNIT – I

III (a) Draw and explain the architecture of Intel 8086. (10)

(b) Describe minimum mode configuration of 8086 with figure. (5)

OR

IV (a) Explain the Register organization of 8086 with figure. (10)

(b) Describe maximum mode configuration of 8086 with figure. (5)

UNIT – II

V Explain addressing modes of 8086 with one example each. (15)

OR

VI (a) Draw and explain interrupt vector table in 8086. (10)

(b) Describe the sources of interrupts in 8086. (5)

UNIT- III

VII Draw and explain the internal architecture of Pentium processor. (15)

OR

VIII (a) Explain the architecture of 80386 with figure. (10)

(b) List the features of Pentium – Pro processor. (5)

UNIT - IV

IX (a) Draw and explain the internal architecture of Core 2 Duo. (10)

(b) List the important technological features of IA processor. (5)

OR

X (a) Differentiate between Core i 3, i 5 and i 7 processors. (10)

(b) Describe the concept of hyper threading technology. (5)
